Interference Search

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	13	((splitter$1 or de$1multiplexer$1) with (chassis or box or enclosure$1 or casing$1 or hous$3) and (PCB or circuit adj board$1) and ($4communication$1 or $4phone)).clm.	US-PGPUB	OR	ON	2007/04/24 10:41
L5	6	L1 and (normal$2 or perpendicular$2 or higher or lower).clm.	US-PGPUB	OR	ON	2007/04/24 10:42
L6	3	L5 and ((PCB or circuit adj board$1) with (extend$4 or extens$3 or extension) with (higher or lower or beyond)).clm.	US-PGPUB	OR	ON	2007/04/24 10:30
L7	388	379/325-332,399.01-413.04.ccls.	US-PGPUB	OR	ON	2007/04/24 10:34
L8	7	L7 and ((splitter$1 or de$1multiplexer$1) with (chassis or box or enclosure$1 or casing$1 or hous$3) and (PCB or circuit adj board$1) and ($4communication$1 or $4phone)).clm.	US-PGPUB	OR	ON	2007/04/24 10:39
L9	2	L8 and (normal$2 or perpendicular$2 or higher or lower).clm.	US-PGPUB	OR	ON	2007/04/24 10:40
L10	1	L9 and ((PCB or circuit adj board$1) with (extend$4 or extens$3 or extension) with (higher or lower or beyond)).clm.	US-PGPUB	OR	ON	2007/04/24 10:32
L11	177	(379/325-332,399.01-413.04 439/512-522,540.1-544 361/719, 730,742,788,796,826).ccls. and @ad<"20011110" and @ad>"20001110"	US-PGPUB	OR	ON	2007/04/24 10:39
L14	6	L11 and ((splitter$1 or de$1multiplexer$1) with (chassis or box or enclosure$1 or casing$1 or hous$3) and (PCB or circuit adj board$1) and ($4communication$1 or $4phone)).clm.	US-PGPUB	OR	ON	2007/04/24 10:41
L15	2	L14 and (normal$2 or perpendicular$2 or higher or lower).clm.	US-PGPUB	OR	ON	2007/04/24 10:42

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	314	(splitter$1 or de$1multiplexer$1) with (chassis or box or enclosure$1 or casing$1 or hous$3) and (PCB or circuit adj board$1) and $4communication$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:04
L6	33	L1 and card adj edge adj connector$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 08:36
L7	243	L1 and (normal$2 or perpendicular$2)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:05
L8	9	L7 and (PCB or circuit adj board$1) with extend$3 with (higher or lower or beyond)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:05
L9	167	379/413.04.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:04
L10	6	L9 and (splitter$1 or de$1multiplexer$1) with (chassis or box or enclosure$1 or casing$1 or hous$3) and (PCB or circuit adj board$1) and $4communication$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:10
L11	4	L10 and (normal$2 or perpendicular$2)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:11

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L12	2	L11 and (PCB or circuit adj board$1) with (extend$4 or extens$3 or extension) with (higher or lower or beyond)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:12
L13	5032	379/325-332,399.01-413.04.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:09
L15	26	L13 and (splitter$1 or de$1multiplexer$1) with (chassis or box or enclosure$1 or casing$1 or hous$3) and (PCB or circuit adj board$1) and ($4communication$1 or $4phone)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:13
L16	17	L15 and (normal$2 or perpendicular$2)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:14
L17	5	L16 and (PCB or circuit adj board$1) with (extend$4 or extens$3 or extension) with (higher or lower or beyond)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:14
L18	36	"379"/$.ccls. and (splitter$1 or de$1multiplexer$1) with (chassis or box or enclosure$1 or casing$1 or hous$3) and (PCB or circuit adj board$1) and ($4communication$1 or $4phone)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:16
L19	24	L18 and (normal$2 or perpendicular$2)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:17

L20	6	L19 and (PCB or circuit adj board$1) with (extend$4 or extens$3 or extension) with (higher or lower or beyond)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:17
L21	2849	439/512-522,540.1-544.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:15
L22	1	L21 and (splitter$1 or de$1multiplexer$1) with (chassis or box or enclosure$1 or casing$1 or hous$3) and (PCB or circuit adj board$1) and ($4communication$1 or $4phone)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:26
L23	1	L22 and (normal$2 or perpendicular$2)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:27
L24	0	L23 and (PCB or circuit adj board$1) with (extend$4 or extens$3 or extension) with (higher or lower or beyond)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:27
L25	4198	361/719,730,742,788,796,826.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:28
L26	8	L25 and (splitter$1 or de$1multiplexer$1) with (chassis or box or enclosure$1 or casing$1 or hous$3) and (PCB or circuit adj board$1) and ($4communication$1 or $4phone)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:29

L27	7	L26 and (normal$2 or perpendicular$2)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:29
L28	1	L27 and (PCB or circuit adj board$1) with (extend$4 or extens$3 or extension) with (higher or lower or beyond)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:30
L29	51437	361/601-837.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:28
L30	24	L29 and (splitter$1 or de$1multiplexer$1) with (chassis or box or enclosure$1 or casing$1 or hous$3) and (PCB or circuit adj board$1) and ($4communication$1 or $4phone)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:37
L31	21	L30 and (normal$2 or perpendicular$2)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:38
L32	2	L31 and (PCB or circuit adj board$1) with (extend$4 or extens$3 or extension) with (higher or lower or beyond)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:38
L33	22	"439"/$.ccls. and (splitter$1 or de$1multiplexer$1) with (chassis or box or enclosure$1 or casing$1 or hous$3) and (PCB or circuit adj board$1) and ($4communication$1 or $4phone)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:37

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L34	20	L33 and (normal$2 or perpendicular$2)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:38
L35	1	L34 and (PCB or circuit adj board$1) with (extend$4 or extens$3 or extension) with (higher or lower or beyond)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/24 09:39